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RULE						
APPLICANTS Mitsunori Takeshita, Toyama, JAPAN; Tomoyuki Matsuda, Toyama, JAPAN; Mitsuhiro Hirano, Toyama, JAPAN; Akihiro Sato, Toyama, JAPAN; Shinya Morita, Toyama, JAPAN; Toshimitsu Miyata, Toyama, JAPAN; Koji Shibata, Toyama, JAPAN;						
** CONTINUING DATA ***** This application is a 371 of PCT/JP2005/011714 06/27/2005						
** FOREIGN APPLICATIONS ***** JAPAN 2004-205577 07/13/2004						
** IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 06/20/2008						
Foreign Priority claimed <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No 35 USC 119(a-d) conditions met <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No Verified and Acknowledged <u>/NATHAN K FORD/</u> Examiner's Signature		<input type="checkbox"/> Met after Allowance Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWINGS 4	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 3
ADDRESS KRATZ, QUINTOS & HANSON, LLP 1420 K Street, N.W. 4th Floor WASHINGTON, DC 20005 UNITED STATES						
TITLE Substrate Processing Apparatus and Manufacturing Method for a Semiconductor Device						
FILING FEE RECEIVED 1030	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:			<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		